Stephen Christopher DeMarco

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My skill set covers a broad range in the hardware and software disciplines, with extensive experience in digital system architectural formulation and behavioral modeling and simulation using Verilog and VHDL, FPGA-based rapid-prototyping in Verilog and VHDL using Altera CPLD/FPGA tools, logic-level synthesis using Synopsys, digital/analog implementation at transistor level with circuit simulation using Hspice and Spectre, and IC layout, verification, DRC/LVS, and fabrication using the Cadence design tools. My past work includes some analog IC design exposure with a strong desire to broaden that experience. I have performed multi-level PCB design (using OrCAD schematic capture and layout tools) with related exposure to board level design using discrete ICs.

For employers in search of software skills, I offer experience with HTML, Coldfusion, SQL, JSP, Java Servlets, and Javascript (for E-commerce or database-driven website design), C++, Visual Basic, and Java experience for application development, C and Fortran for scientific computing, and assembly language (or C) for embedded development in microcontroller systems. I am currently broadening these skills with a study of Flash programming.

In addition to these skill in the hardware and software disciplines, I have many years of machine shop training on equipment including vertical milling machines, metal lathes, band saws, drilling presses, and computer numerically controlled (CNC) equipment, including gerber (GM code) programming.

I would very much like to put my talents and interests to work for a company with relevant needs. I am available for interviews upon request with references readily available. I can be easily reached by email or cellular phone as given below.

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